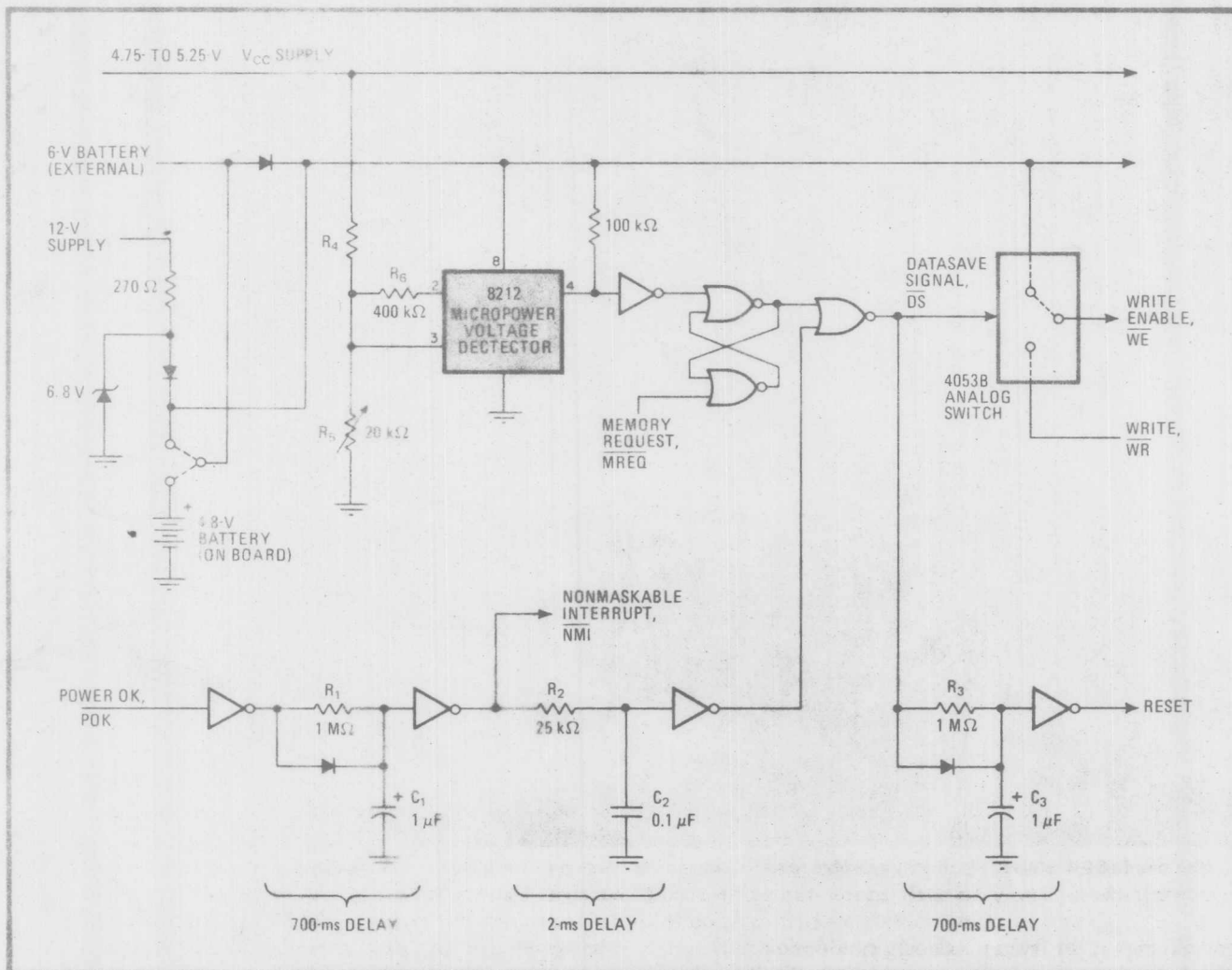
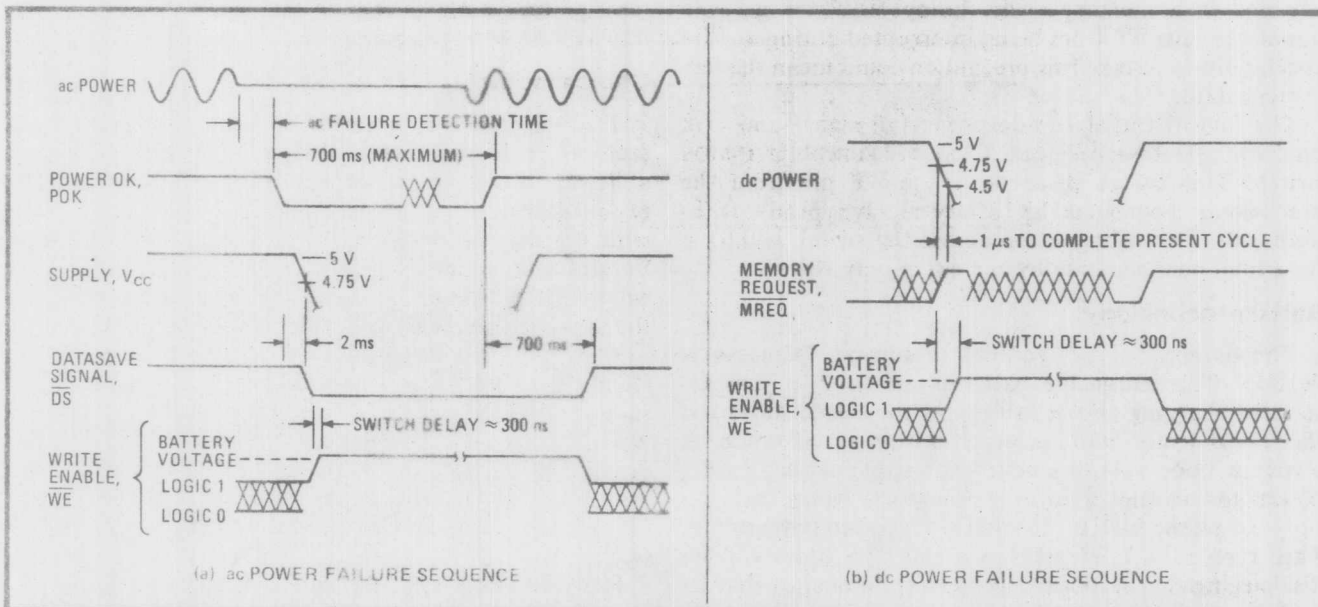




3. Energy watchdog. This circuitry monitors and reacts to both ac and dc power failures. In either case, a Datasave signal, $\overline{DS}$, switches supply current into the write-enable line, $\overline{WE}$. Other components make for an orderly microprocessor shutdown and restart.



4. Power failure timing. The waveforms in (a) apply if line current disappears, whereas those in (b) are valid if dc power goes away. Both sets of signals relate to Fig. 3. After delays for system housekeeping, the write-enable line is raised to the V_{cc} level.